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PATENT
Docket No. 15436.247.35.1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Certificate

DiJaili et al.

AUG 18 2008

of Correction

Serial No.: 10/020,527

Filed: Dec. 14, 2001

For: OPTICAL CROSSBAR USING LASING
SEMICONDUCTOR OPTICAL AMPLIFIERS

Examiner: Mark Hellner

Patent No.: 7,046,434 B1

Issue Date: May 16, 2006

Art Unit
3663

REQUEST FOR A CERTIFICATE OF CORRECTION
UNDER 37 C.F.R. § 1.323

Attn: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 C.F.R. § 1.323 which implement 35 U.S.C. § 255, the Commissioner is respectfully requested to issue a Certificate of Correction to correct mistakes in the above-identified patent. The enclosed Patent Office Form 1050 reflects the desired corrections. Also enclosed please find a check in the amount of \$100.00 to cover the costs for issuance of this certificate.

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Patent Application
100.00 OP
AUG 18 2008

The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment to Deposit Account No. 23-3178. Duplicate copies of this letter are enclosed.

Dated this 6 day of August, 2008.

Respectfully submitted,

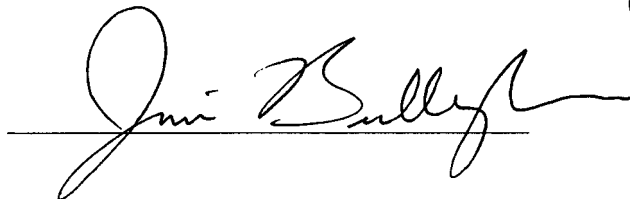


R. Burns Israelson
Attorney for Patentee
Registration No. 42,685
Customer No. 022913

RBI:jsb
Docket No. 15436.247.35.1
Certificate of Correction Documents (with check)

CERTIFICATE OF DEPOSIT UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 12 day of August, 2008.



Transmitted: REQUEST FOR A CERTIFICATE OF CORRECTION UNDER 37 C.F.R. § 1.323,
PTO Form 1050 and Check No. 15436 for \$100.00

159325

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Patent Publication

AUG 18 2008



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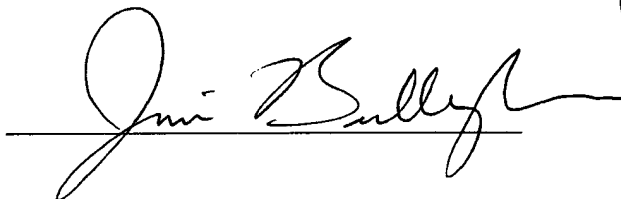


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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 7,046,434 B1

Page 1 of 6

APPLICATION NO. : 10/020,527

ISSUE DATE : May 16, 2006

INVENTOR(S) : DiJaili et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page

Item 56, Page 2, Other Publications, Column 1, Line 42, change "Jong" to --Jeong--

Drawings

Sheet 2, change "Figure 2" to the figure depicted herein below, wherein the reference "201" has been removed.

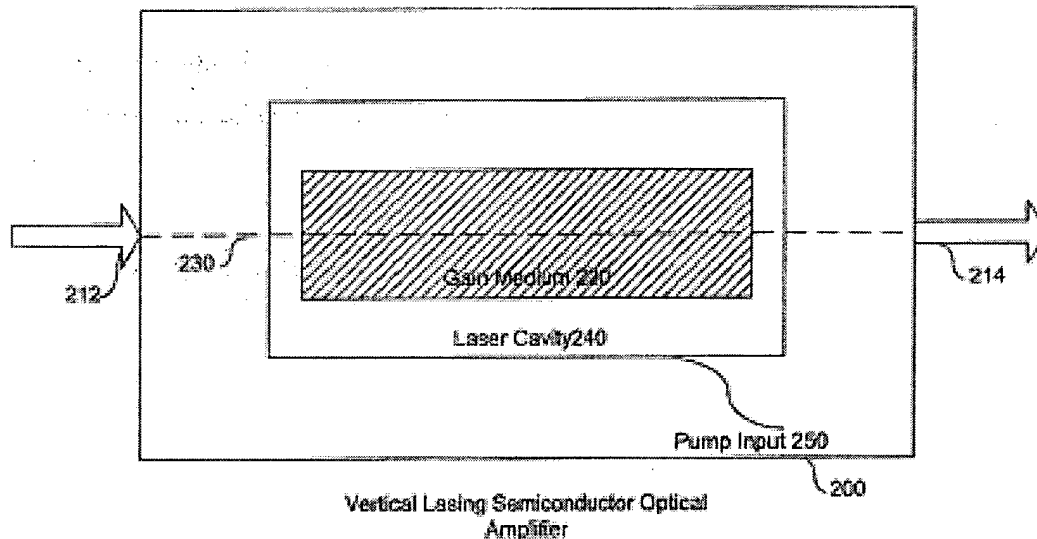


Figure 2

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PLATE 1000000000

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R. Burns Israelsen, Esq., WORKMAN NYDEGGER
1000 Eagle Gate Tower
60 East South Temple
Salt Lake City, Utah 84111

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If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

UNITED STATES PATENT AND TRADEMARK OFFICE

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PATENT NO. : 7,046,434 B1

Page 2 of 6

APPLICATION NO. : 10/020,527

ISSUE DATE : May 16, 2006

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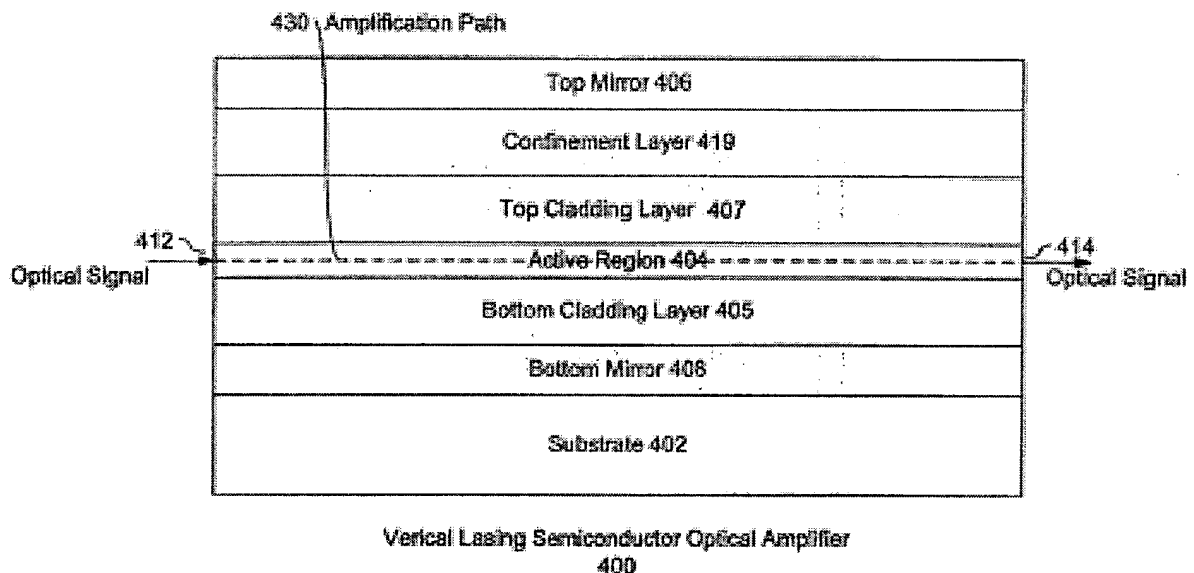


Figure 4C

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1000 Eagle Gate Tower
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Salt Lake City, Utah 84111

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PATENT NO. : 7,046,434 B1

Page 3 of 6

APPLICATION NO. : 10/020,527

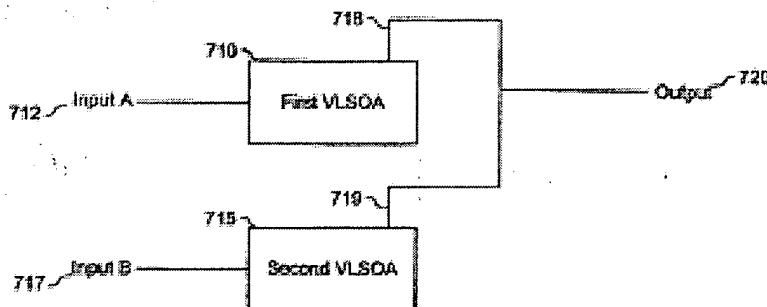
ISSUE DATE : May 16, 2006

INVENTOR(S) : DiJaili et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Drawings

Sheet 11, change "Figure 7A" to the figure depicted herein below, wherein the NAND gate has been labeled --700--



Optical NAND Gate 700

Figure 7A

MAILING ADDRESS OF SENDER (Please do not use customer number)

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60 East South Temple
Salt Lake City, Utah 84111

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PATENT NO. : 7,046,434 B1

Page 4 of 6

APPLICATION NO. : 10/020,527

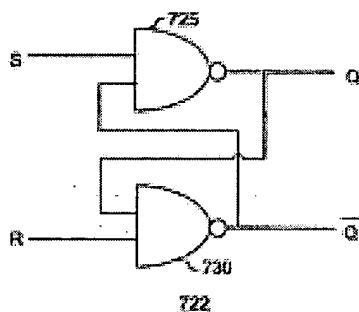
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Sheet 12, change "Figure 7B" to the figure depicted herein below, wherein the 2nd and 3rd row of the outputs have been change to --1-- and --0--, and --0-- and --1--, respectively.



| S | R | Q | Q̄ | |
|---|---|---|----|--------------|
| 0 | 0 | 1 | 1 | Not Used |
| 0 | 1 | 1 | 0 | |
| 1 | 0 | 0 | 1 | |
| 1 | 1 | 0 | 1 | Memory State |
| | | 1 | 0 | |

Optical Flip-Flop

Figure 7B

MAILING ADDRESS OF SENDER (Please do not use customer number)

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1000 Eagle Gate Tower

60 East South Temple

Salt Lake City, Utah 84111

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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 7,046,434 B1

Page 5 of 6

APPLICATION NO. : 10/020,527

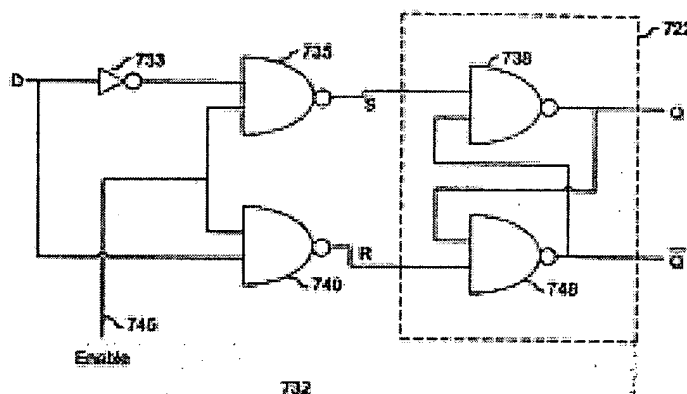
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INVENTOR(S) : DiJaili et al.

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Drawings

Sheet 13, change "Figure 7C" to the figure depicted herein below, wherein the 3rd and 4th row of the outputs have been change to --1-- and --0--, and --0-- and --1--, respectively.



| D | Enable | Q | Q |
|---|--------|--------------|---|
| 0 | 0 | Memory State | |
| 1 | 0 | Memory State | |
| 0 | 1 | 1 | 0 |
| 1 | 1 | 0 | 1 |

Optical Buffer Element
Figure 7C

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UNITED STATES PATENT AND TRADEMARK OFFICE

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PATENT NO. : 7,046,434 B1

Page 6 of 6

APPLICATION NO. : 10/020,527

ISSUE DATE : May 16, 2006

INVENTOR(S) : DiJaili et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4

Line 53, change "115" to --110--

Column 11

Line 5, change "421" to --412--

Line 24, change "410" to --310--

Line 25, change "220" to --320--

Line 40-41, change "10 dB" to --20 dB--

Line 43, change "30 dB" to --60 dB--

Column 12

Line 17, change "510" to --110--

Column 15

Line 18, change "FIG. 17B" to --FIG. 7B--

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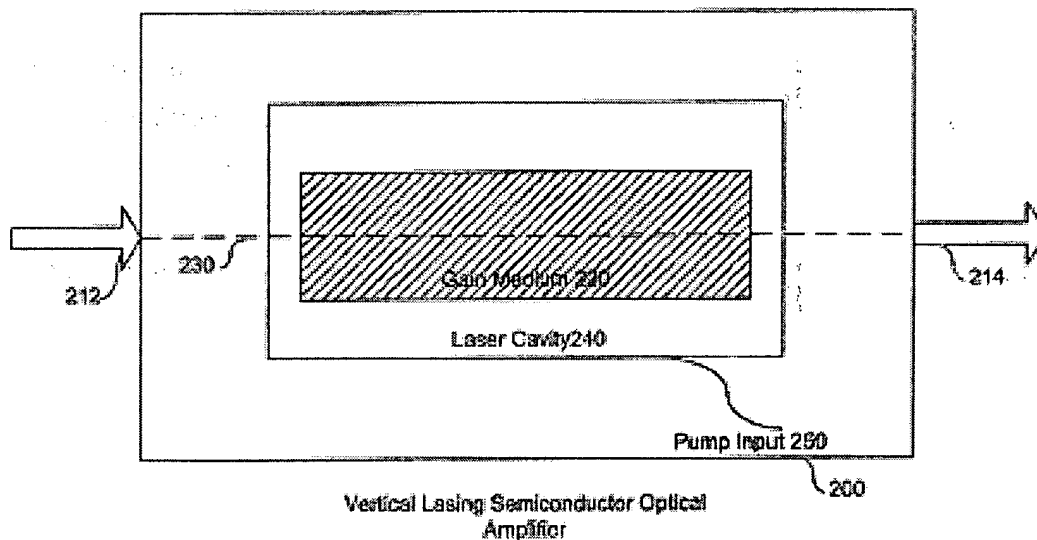


Figure 2

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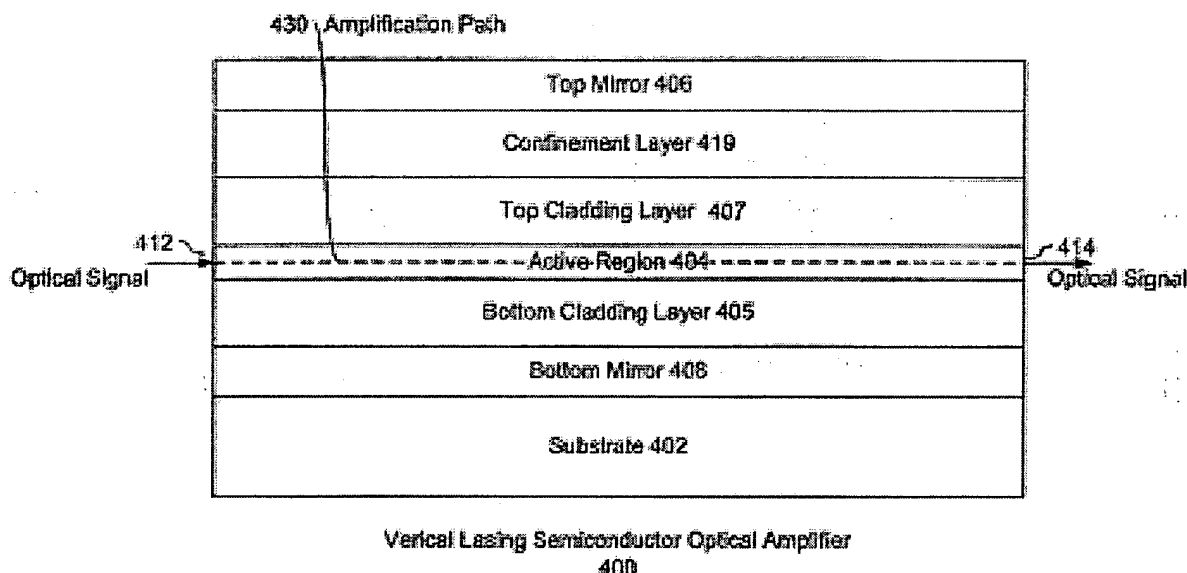


Figure 4C

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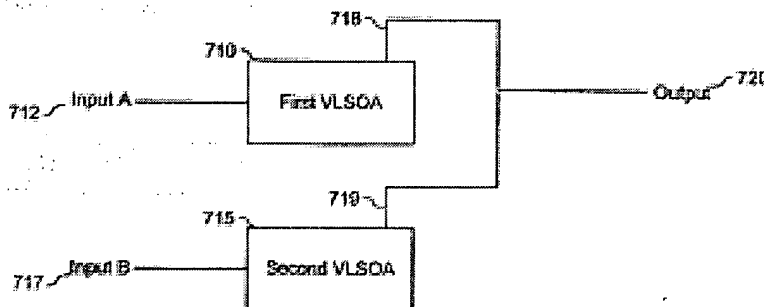
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Optical NAND Gate 700

Figure 7A

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60 East South Temple
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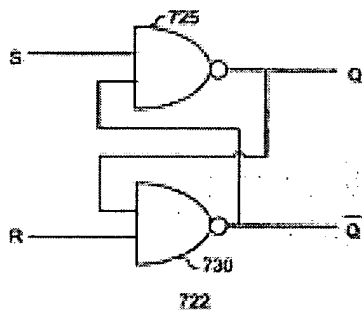
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| S | R | Q | Q-bar | |
|---|---|---|-------|--------------|
| 0 | 0 | 1 | 1 | Not Used |
| 0 | 1 | 1 | 0 | |
| 1 | 0 | 0 | 1 | |
| 1 | 1 | 0 | 1 | Memory State |
| | | 1 | 0 | |

Optical Flip-Flop

Figure 7B

MAILING ADDRESS OF SENDER (Please do not use customer number)

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1000 Eagle Gate Tower

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PATENT
AUG 1 8 2003

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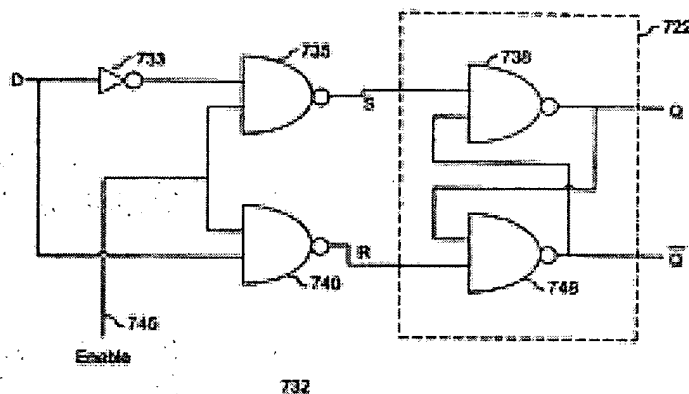
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|---|--------|--------------|---|
| 0 | 0 | Memory State | |
| 1 | 0 | Memory State | |
| 0 | 1 | 1 | 0 |
| 1 | 1 | 0 | 1 |

Optisol Buffer Element
Figure 7C

MAILING ADDRESS OF SENDER (Please do not use customer number)

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Salt Lake City, Utah 84111

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PATENT NO. : 7,046,434 B1

Page 6 of 6

APPLICATION NO. : 10/020,527

ISSUE DATE : May 16, 2006

INVENTOR(S) : DiJaili et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4

Line 53, change "115" to --110--

Column 11

Line 5, change "421" to --412--

Line 24, change "410" to --310--

Line 25, change "220" to --320--

Line 40-41, change "10 dB" to --20 dB--

Line 43, change "30 dB" to --60 dB--

Column 12

Line 17, change "510" to --110--

Column 15

Line 18, change "FIG. 17B" to --FIG. 7B--

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